



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/058,116	01/29/2002	Mutsumi Kimura	111829	1207

25944 7590 06/27/2003

OLIFF & BERRIDGE, PLC
P.O. BOX 19928
ALEXANDRIA, VA 22320

EXAMINER

MUNSON, GENE M

ART UNIT PAPER NUMBER

2811

DATE MAILED: 06/27/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/58,116

Applicant(s)

M. KIMURA

Examiner

G. MUNSON

Group Art Unit

2811

— The MAILING DATE of this communication appears on the cover sheet beneath the correspondence address —

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE THREE MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, such period shall, by default, expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- ☒ Responsive to communication(s) filed on 29 July 2002
- ☐ This action is **FINAL**.
- ☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

Disposition of Claims

- ☒ Claim(s) 1-15 is/are pending in the application.
- Of the above claim(s) _____ is/are withdrawn from consideration.
- ☒ Claim(s) 1-5 is/are allowed.
- ☒ Claim(s) 6-15 is/are rejected.
- ☐ Claim(s) _____ is/are objected to.
- ☐ Claim(s) _____ are subject to restriction or election requirement

Application Papers

- ☒ The proposed drawing correction, filed on 29 July 2002 is ☒ approved ☐ disapproved.
- ☐ The drawing(s) filed on _____ is/are objected to by the Examiner
- ☐ The specification is objected to by the Examiner.
- ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119 (a)-(d)

- ☒ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119 (a)-(d).

☒ All ☐ Some* ☐ None of the:

☒ Certified copies of the priority documents have been received.

☐ Certified copies of the priority documents have been received in Application No. _____

☐ Copies of the certified copies of the priority documents have been received
in this national stage application from the International Bureau (PCT Rule 17.2(a))

*Certified copies not received: _____

Attachment(s)

- ☒ Information Disclosure Statement(s), PTO-1449, Paper No(s). 7
- ☐ Interview Summary, PTO-413
- ☒ Notice of Reference(s) Cited, PTO-892
- ☐ Notice of Informal Patent Application, PTO-152
- ☐ Notice of Draftsperson's Patent Drawing Review, PTO-948
- ☐ Other _____

Office Action Summary

Art Unit: 2811

Claims 11-15 are rejected under 35 U.S.C. 112, first, second and fourth paragraphs. It is unclear how claims 11-13 and 15 further limit the "semiconductor device" of claims 1 or 7. It is suggested that claims 11-13 and 15 each be put in completed form as independent claims including all limitations of claims 1, 11; 1, 11,12; 7, 13; 1, 11, 12, 15.

The "circuit board" (claims 11, 12) and "shift registers, level shifters, buffer circuits, and analog switches" (claim 13) are not clearly described so as to enable any person skilled in the art to make and use the claimed inventions.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(c) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Art Unit: 2811

Claims 6-10 are rejected under 35 U.S.C. 102 as unpatentable as shown by Inoue. See Figures 8, 16-18, 24.

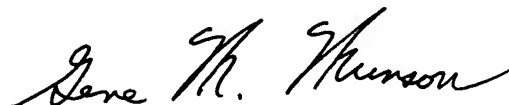
Claims 6-10 are rejected under 35 U.S.C. 103 as unpatentable over Inoue. It would have been obvious that channel regions 30 of Inoue (Figures 8) are "intrinsic semiconductor which is not doped with dopant."

Yamazaki and Yamazaki et al are cited of interest in showing use of thin film transistors with electroluminescence elements.

Claims 1-5 are allowed over the art of record, which does not show nor would have suggested claims 1 and 2 taken as a whole.

G Munson/pj
(703) 308-4925 or 0956

06/24/03



GENE M. MUNSON
EXAMINER
GROUP ART UNIT 2811